

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Timothy M. Lacey et al.  
Serial No.: 09/475,879  
Title: PROGRAMMABLE LOGIC DEVICE  
Filed: December 30, 1999  
Examiner: Tran, A.  
Art Unit: 2819

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**DECLARATION OF TIMOTHY M. LACEY  
UNDER 37 C.F.R. 1.131**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

I, Timothy M. Lacey hereby declare and state:

1. I have been employed by Cypress Semiconductor Corporation since January 16, 1989, where my current job title is New England Design Center Site Director.

2. Prior to March 24, 1999, David L. Johnson and I conceived the invention claimed in the above-identified patent application as shown by the pages of the Ultra39000 Family Architecture Spec attached as Exhibit A. The redacted dates on the Document History Page (i.e., page 98 of 98) show document issue dates which were before March 24, 1999 (see MPEP §715.07, "ESTABLISHMENT OF DATES", allowing allegation that acts referred to occurred prior to a specified date where the actual dates have been redacted).

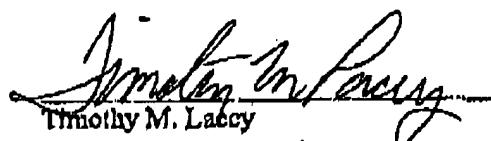
*Declaration*  
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*12/13/01*  
*[Signature]*

3. The invention claimed in the above identified application was diligently reduced to practice as shown by the attached Exhibits B and C. Exhibit B is a copy of a Cypress Semiconductor Invention Disclosure Form for the present invention. The notations in the lower right hand corner of pages 1 through 6 of 6 in Exhibit B show a date of July 14, 1999. Dates pertaining to the conception of the present invention which have been redacted occurred prior to March 24, 1999 (see MPEP §715.07, "ESTABLISHMENT OF DATES", allowing allegation that acts referred to occurred prior to a specified date where the actual dates have been redacted). Exhibit C is a copy of a letter dated September 13, 1999 from Cypress Semiconductor's Corporate Counsel engaging Applicants' representative's law firm to prepare the application filed December 30, 1999.

4. In my opinion, the attached Exhibits A and B corresponding to the Ultra39000 Family Architecture Spec and the Cypress Semiconductor Invention Disclosure Form, respectively, describe the claimed invention and convey information sufficient to enable one skilled in the relevant art to make and use the claimed invention.

5. To the best of my knowledge, no working samples of the Delta 39K family were sent to any party outside Cypress Semiconductor until December 30, 1999 or later.

6. I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that those statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of this application or any patent issuing thereon.

  
Timothy M. Lacey  
12/11/01  
Date